



FIG.1

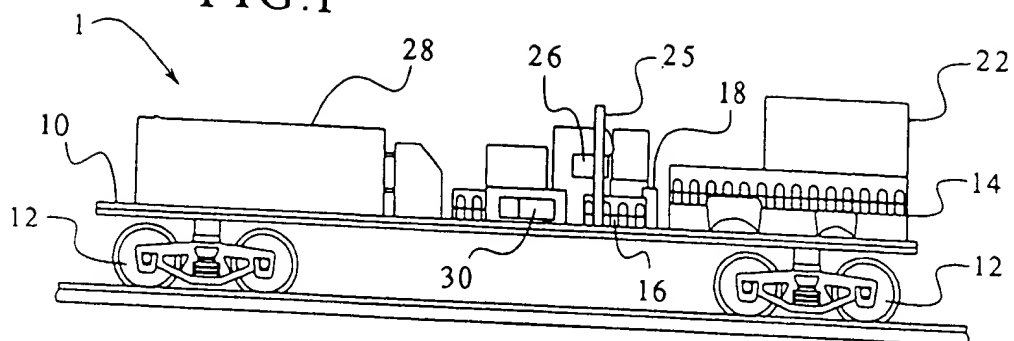


FIG.2

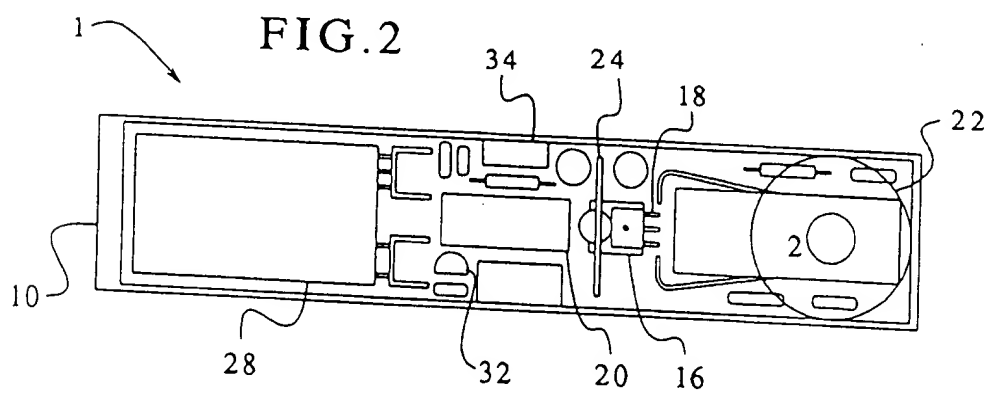
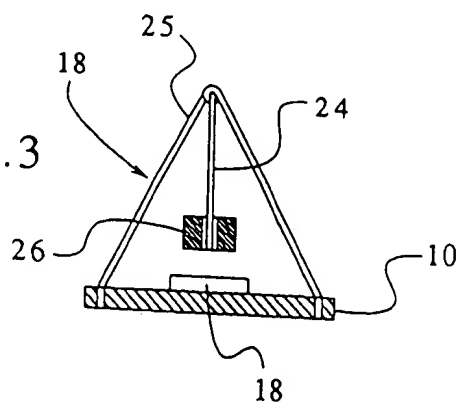
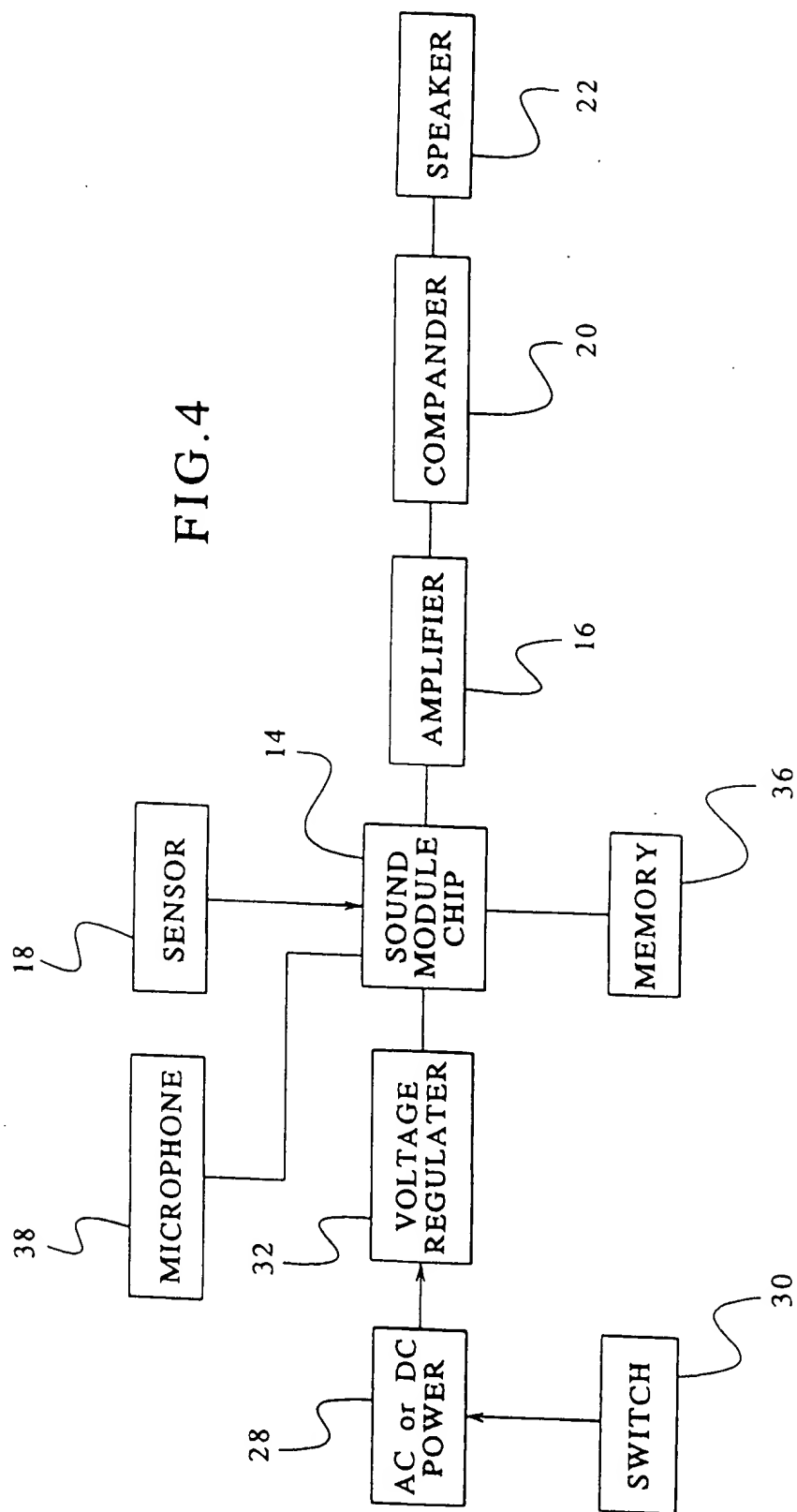


FIG.3





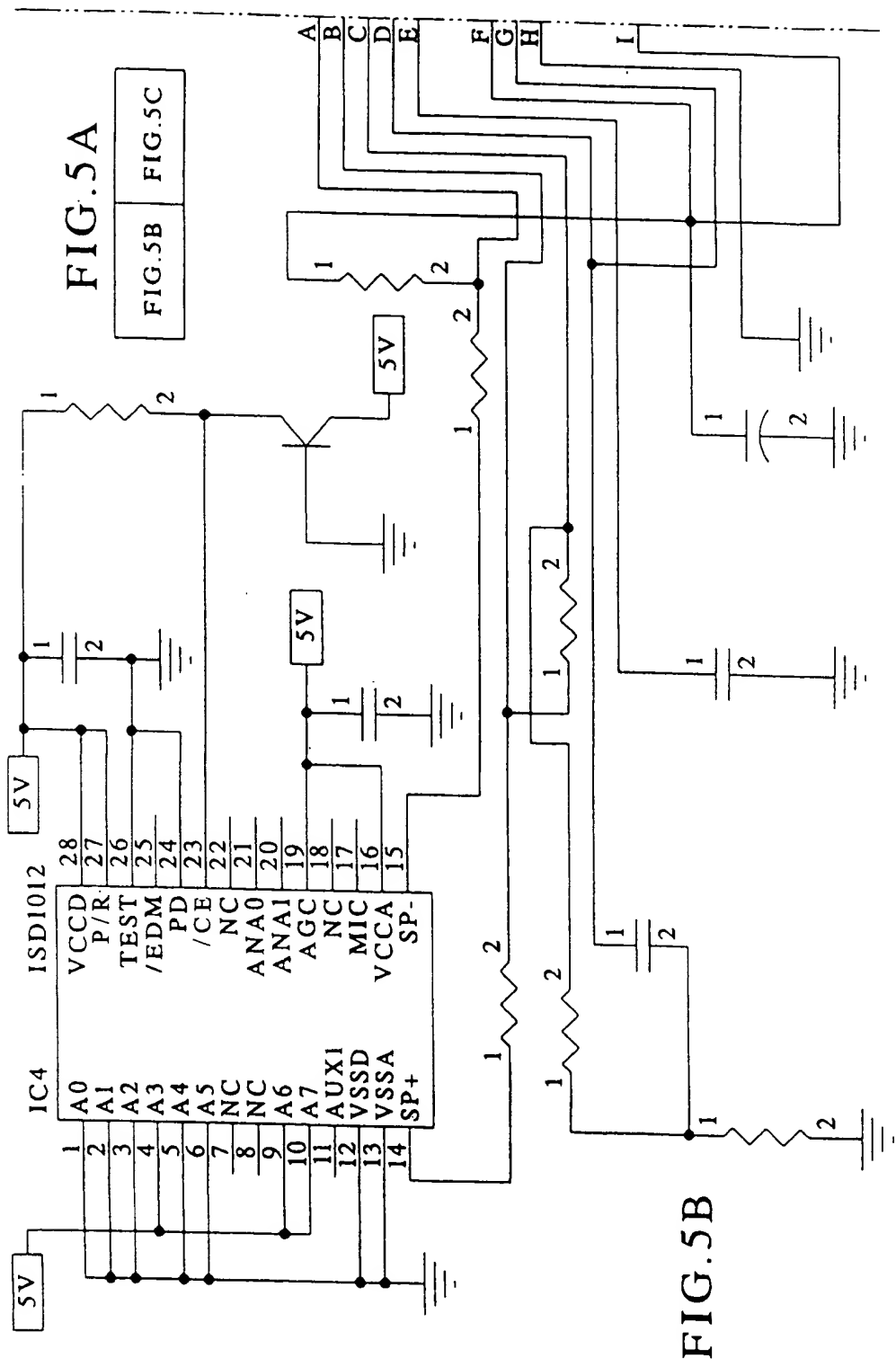


FIG.5A

FIG.5B

FIG.5B

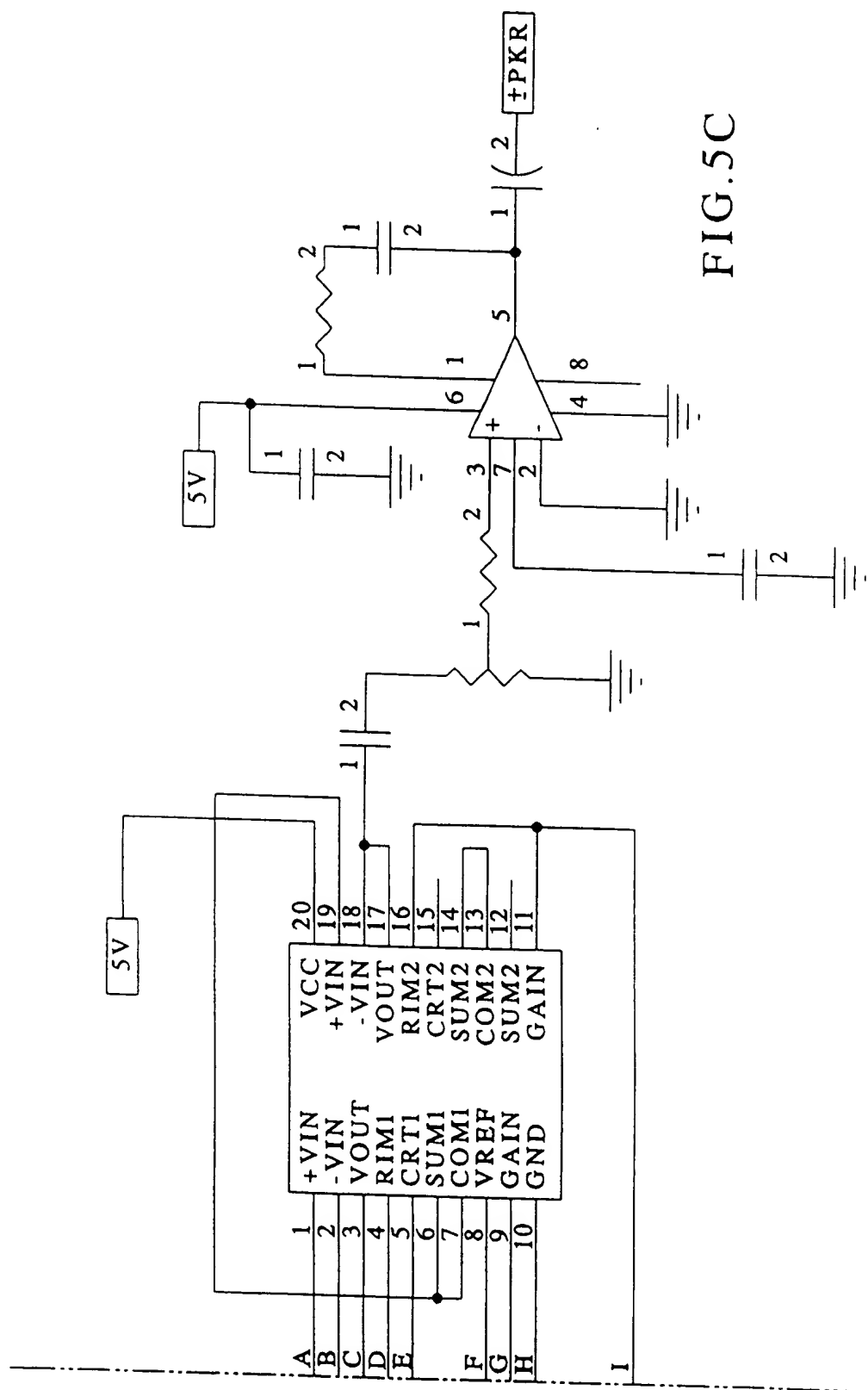


FIG.6

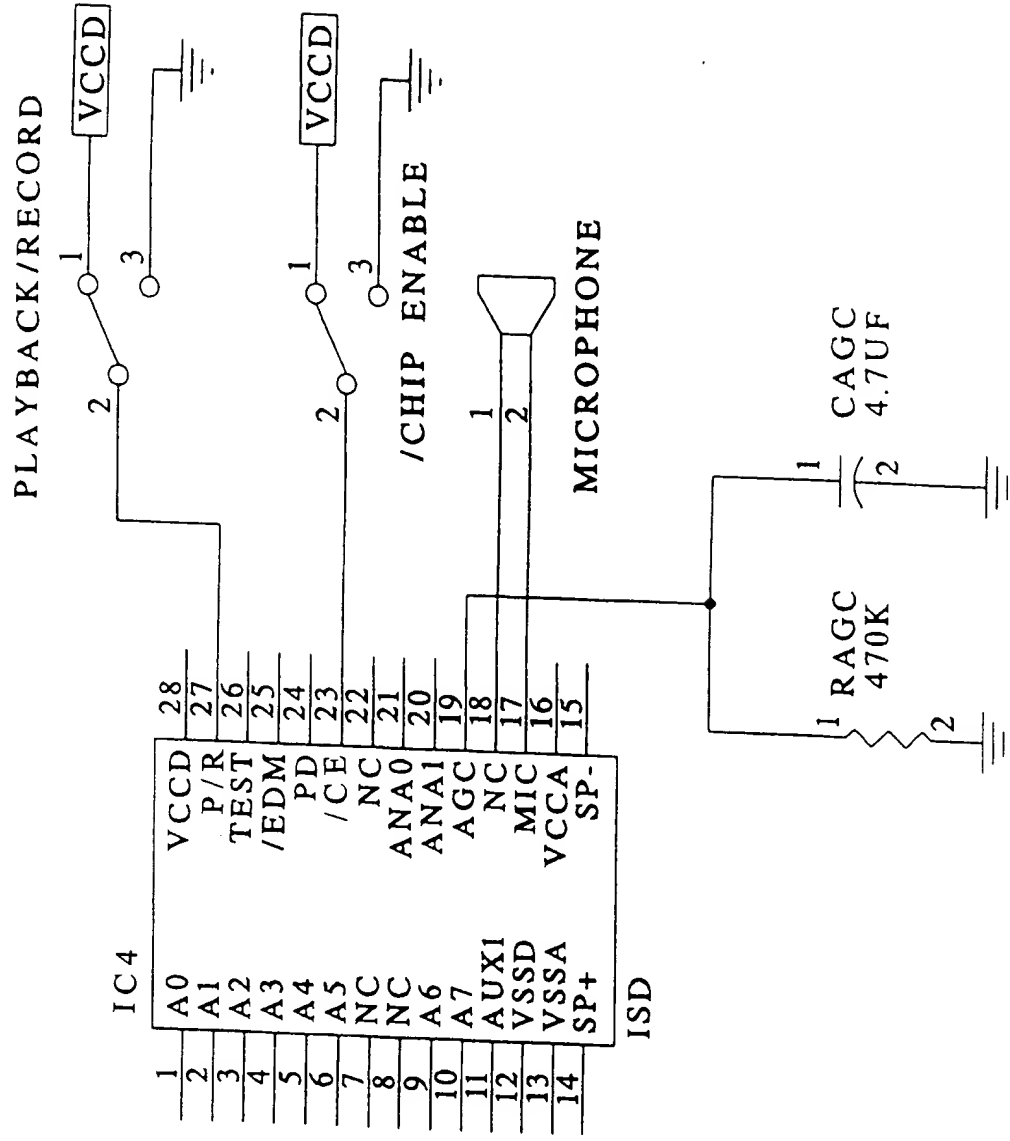


FIG. 7A

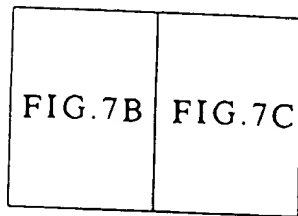


FIG. 7B

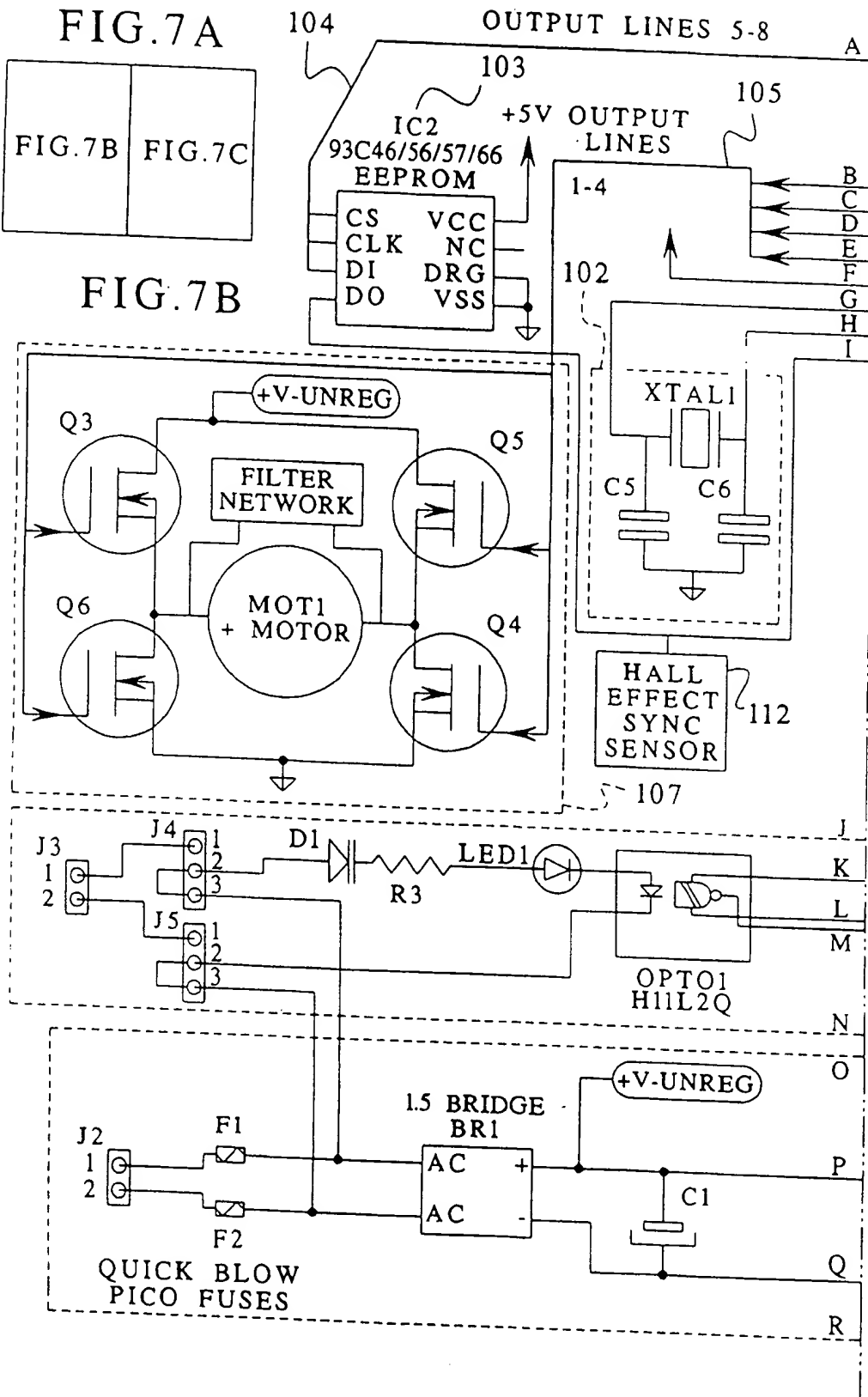


FIG.7C

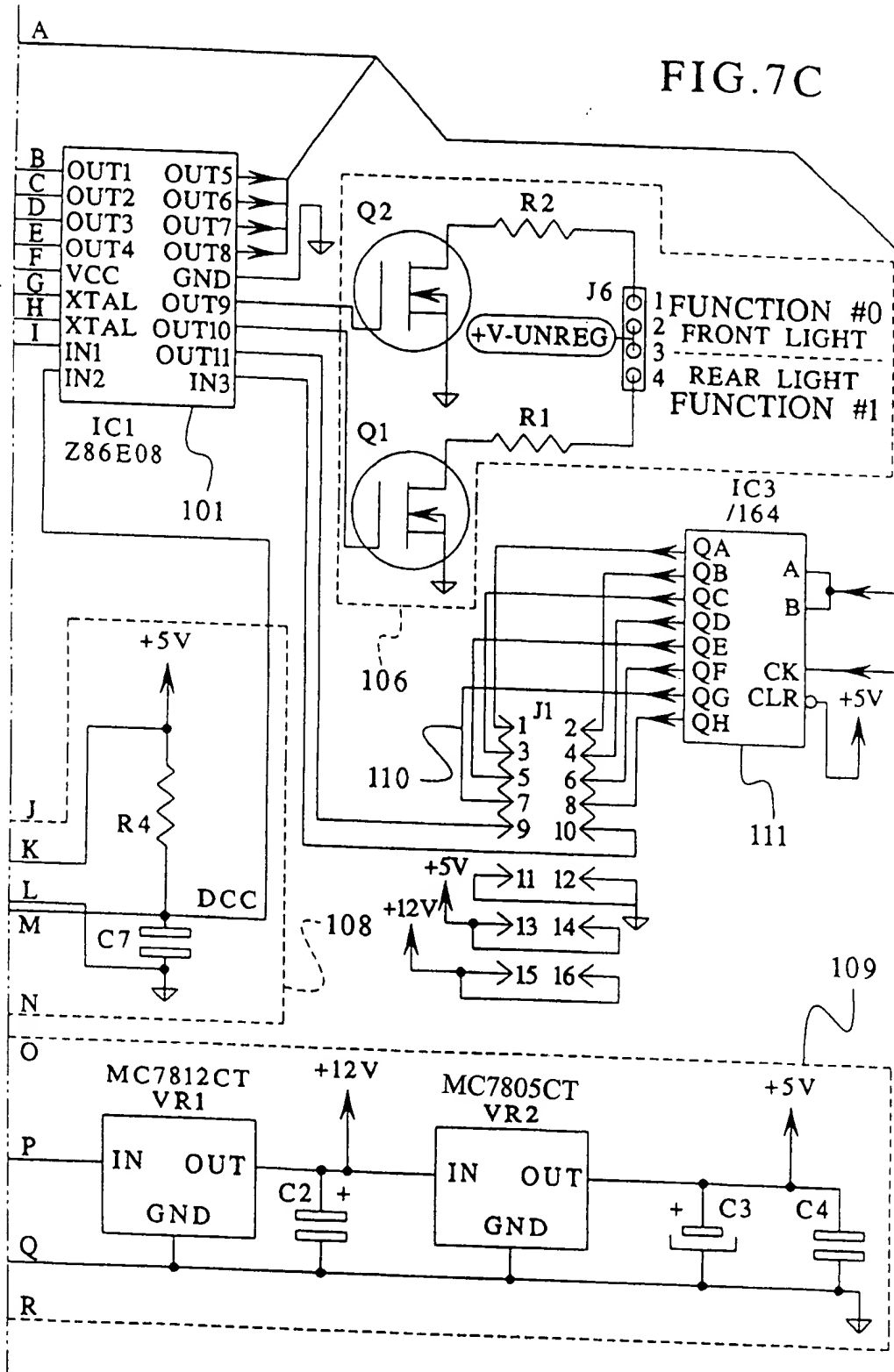


FIG.8A

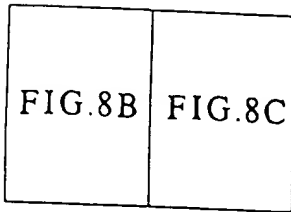
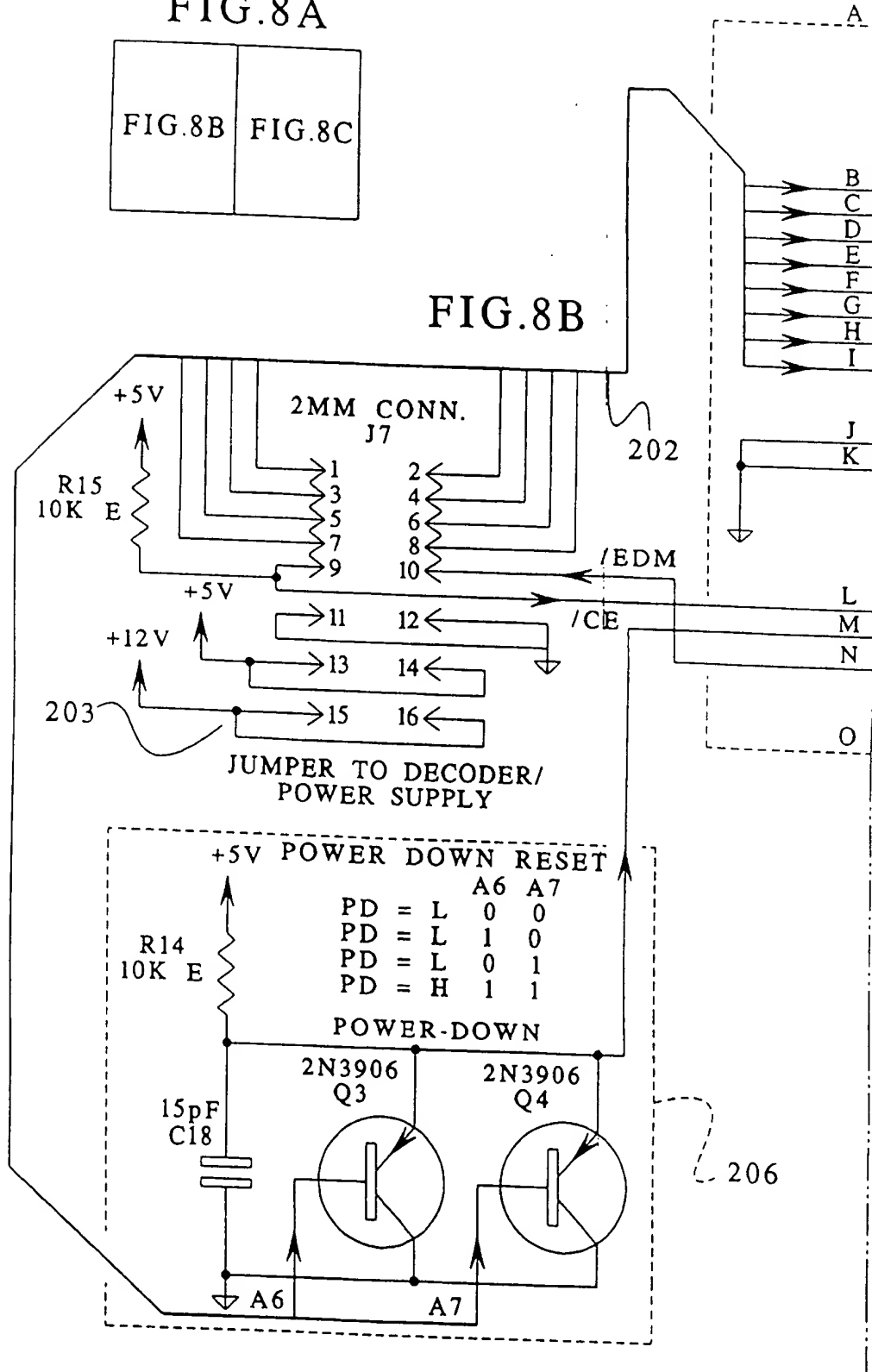


FIG.8B



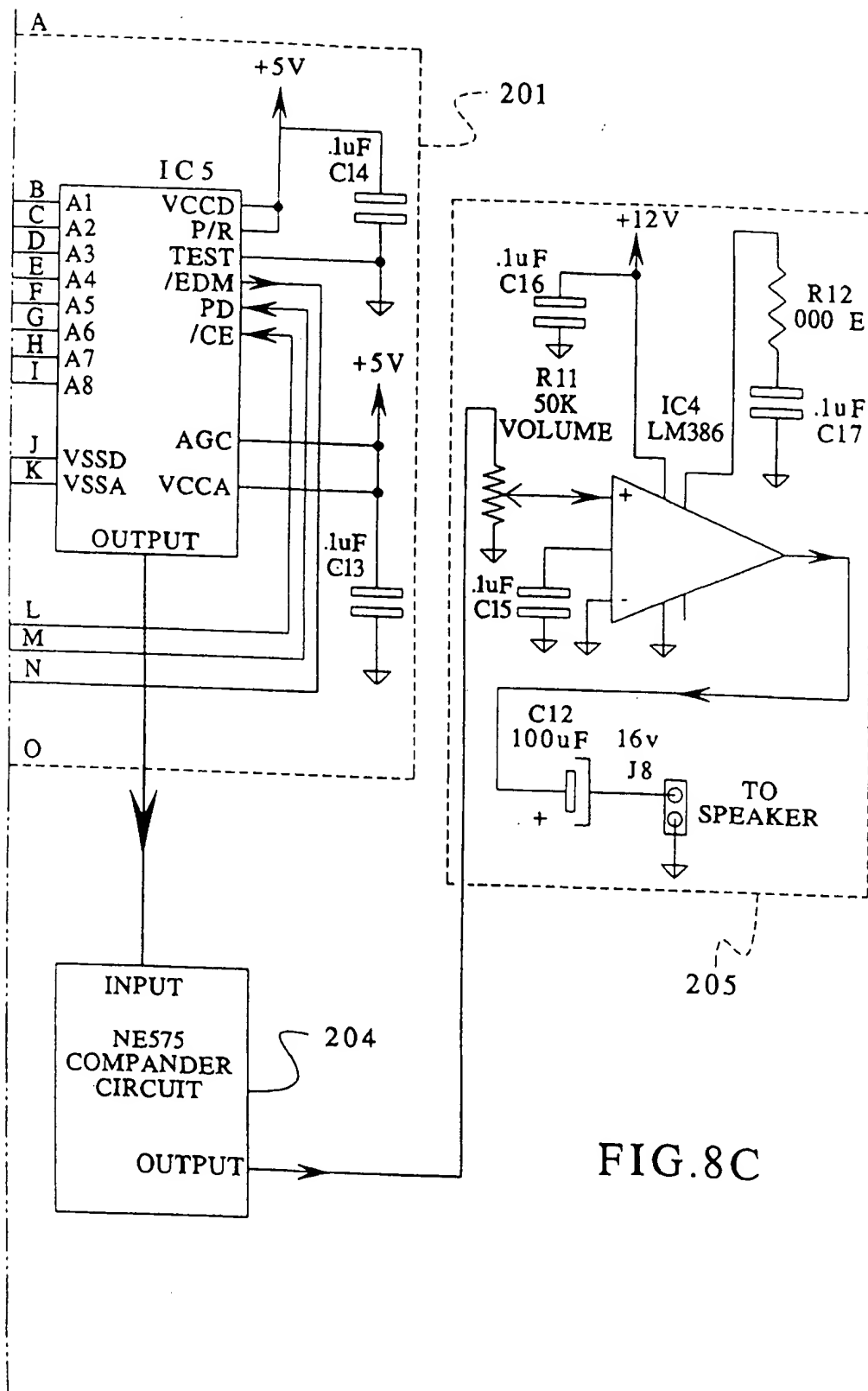


FIG.8C

FIG.9A

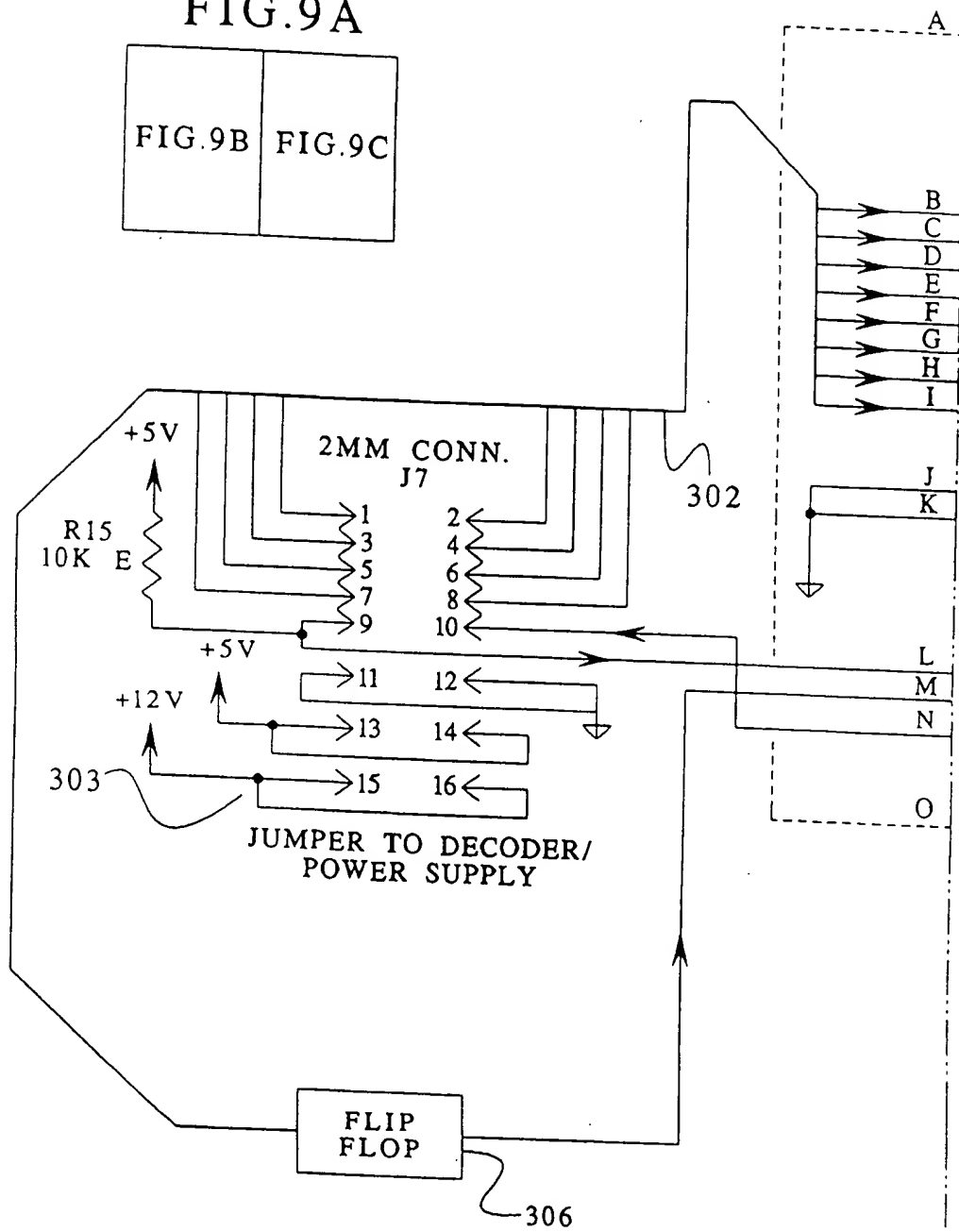


FIG.9B

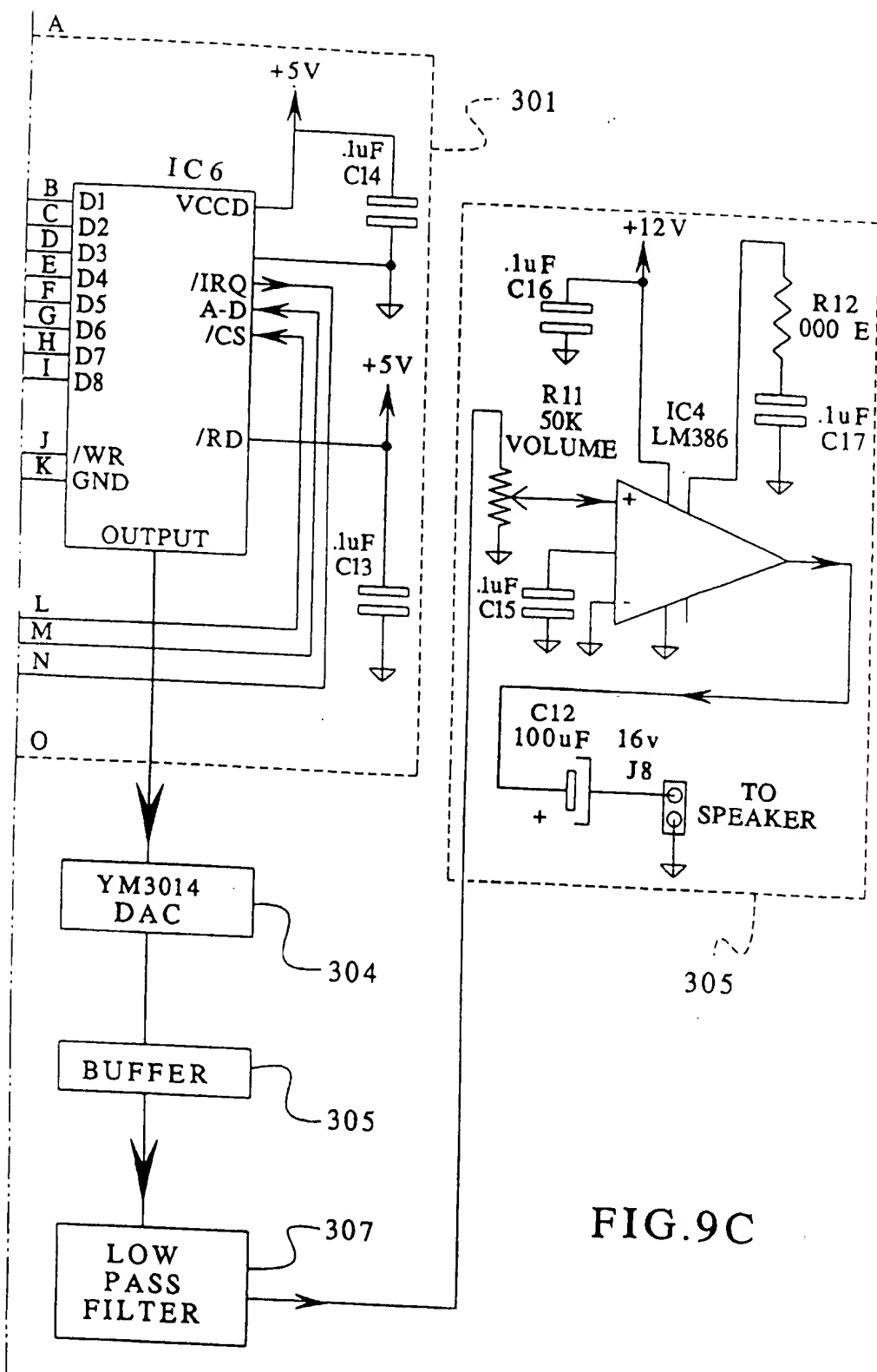


FIG. 9C

FIG.10

DCC BASELINE PACKET 401
 1 = 58us (52us-64us) 402
 0 > 100us (90us-10000us) 403

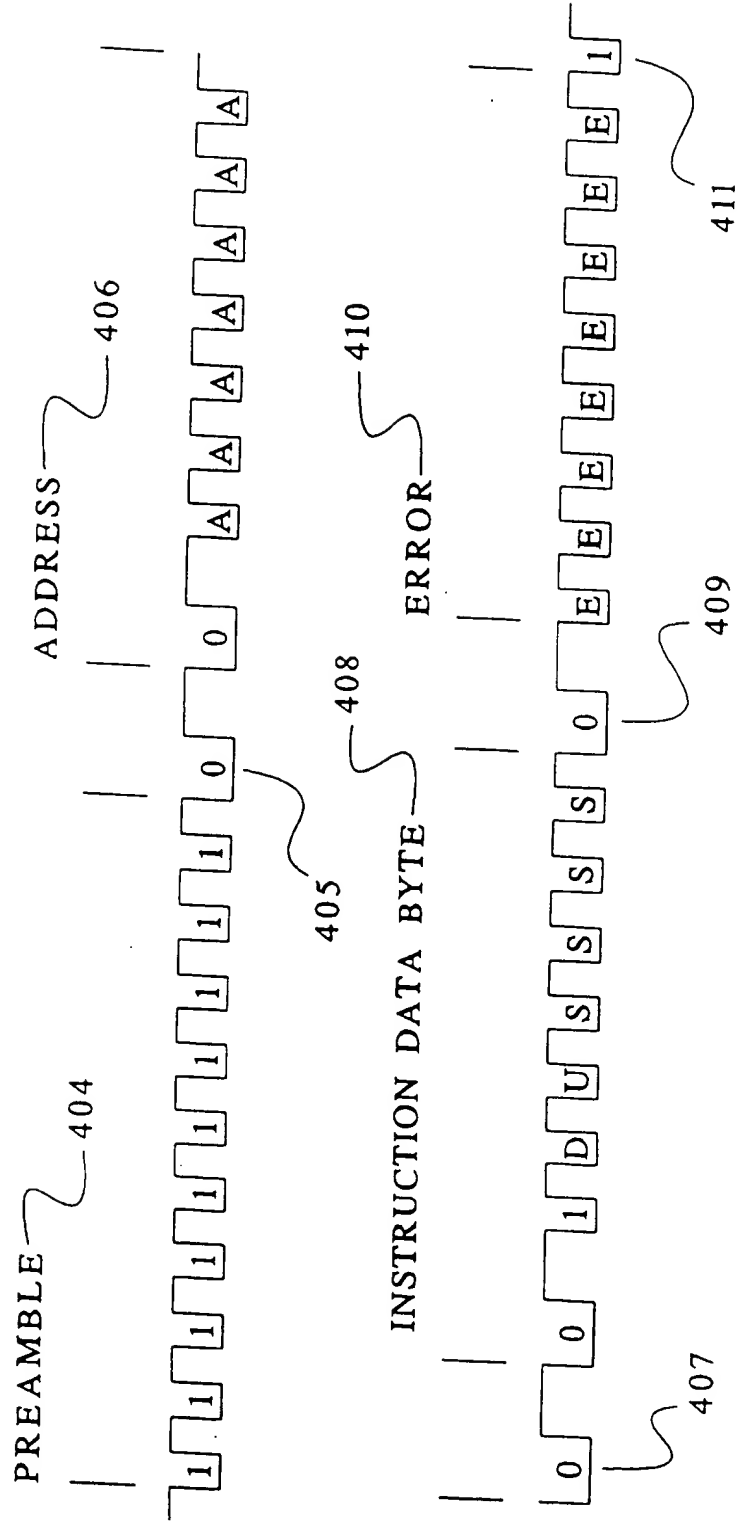


FIG. 11

```
graph TD
    501([START]) --> 502[INITIALIZATION]
    502 --> 503[READ ADDRESS]
    503 --> 504[DCC PREAMBLE RECEPTION]
    504 --> 505[DCC PACKET RECEPTION]
    505 --> 506{IDLE PACKET?}
    506 -- Y --> 503
    506 -- N --> 508{BROADCAST COMMAND?}
    508 -- Y --> 515((A))
    508 -- N --> 509{DECODER & CONSIST INSTRUCTION?}
    509 -- Y --> 516((B))
    509 -- N --> 510{ADVANCED OPERATIONS?}
    510 -- Y --> 517((C))
    510 -- N --> 511{BASELINE SPEED &}
    511 -- Y --> 518((D))
    511 -- N --> 512{DIRECTION? FUNCTION GROUP #1?}
    512 -- Y --> 519((E))
    512 -- N --> 513{ACCESS CONFIGURATION VARIABLES?}
    513 -- Y --> 520((F))
    513 -- N --> 503
    514{ERROR CHECK DATA OK?} -- Y --> 503
    514 -- N --> 514G((G))
    514G --> 503
```

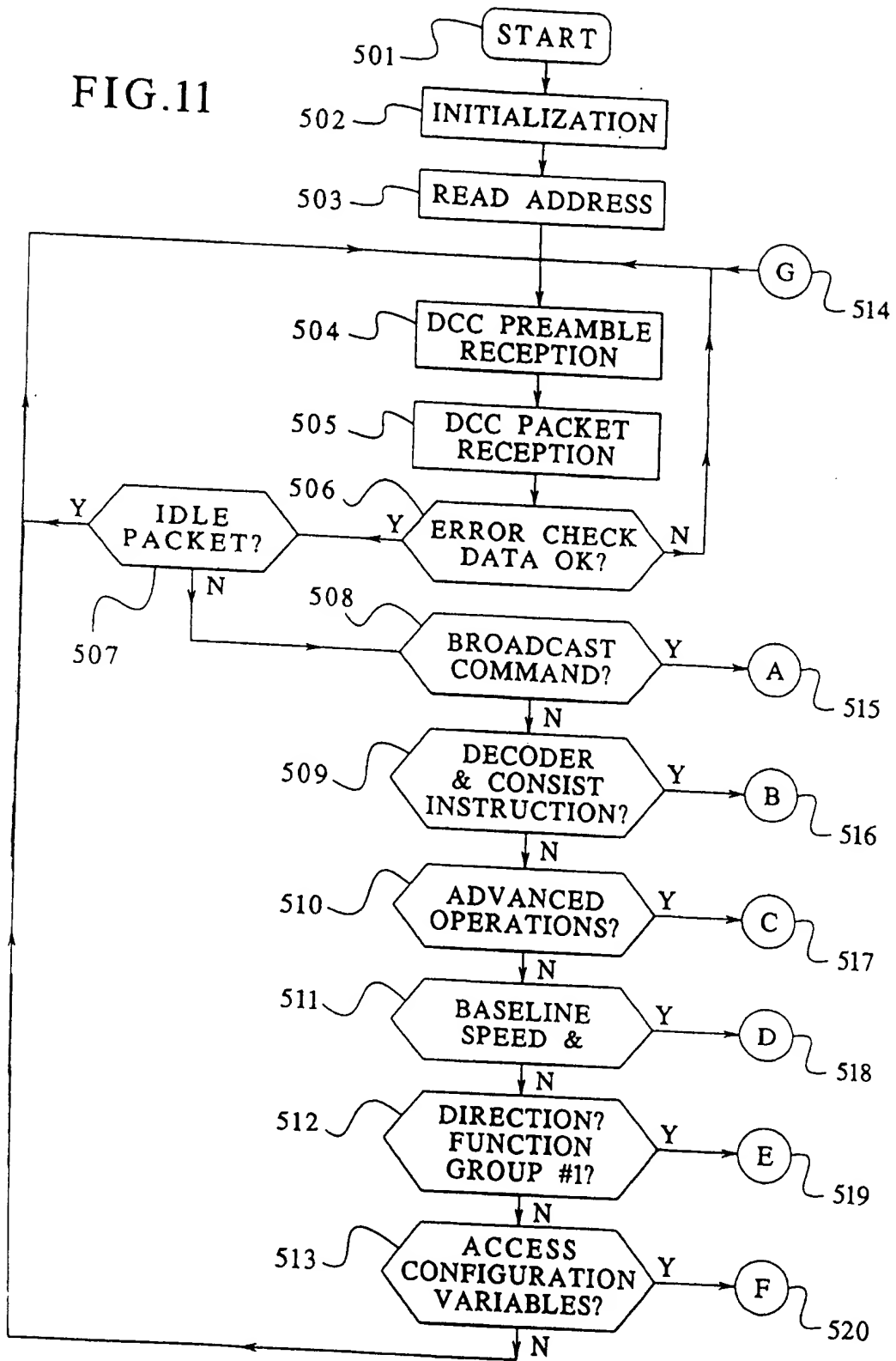


FIG.12

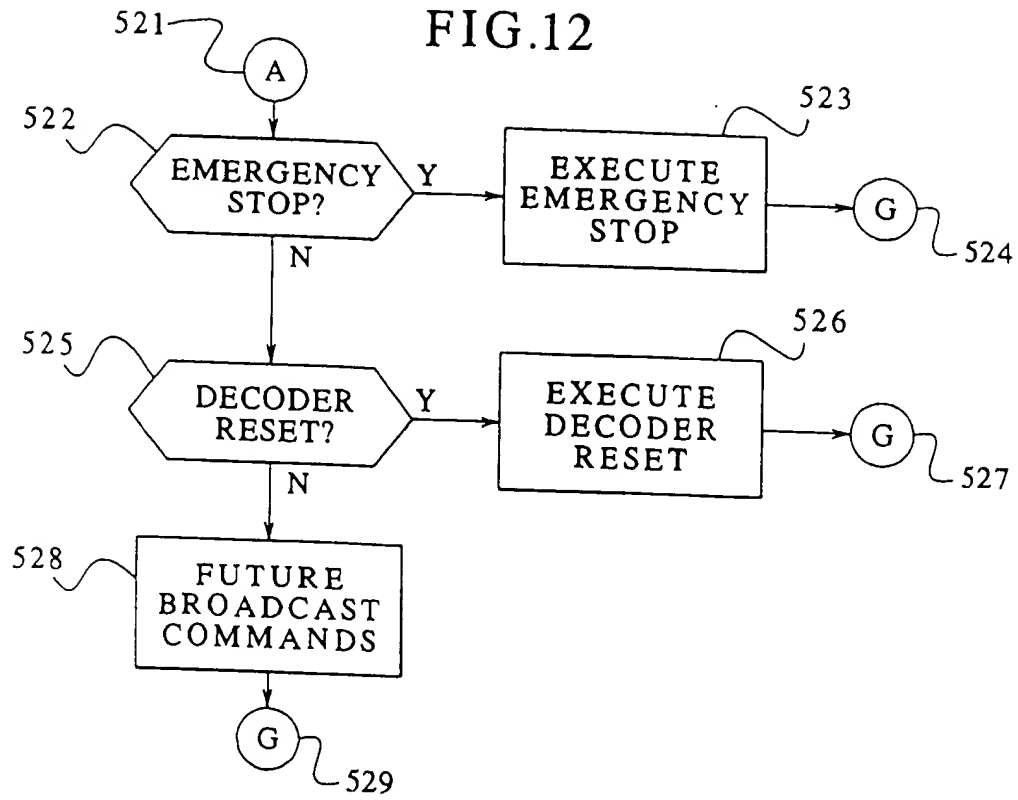


FIG.13

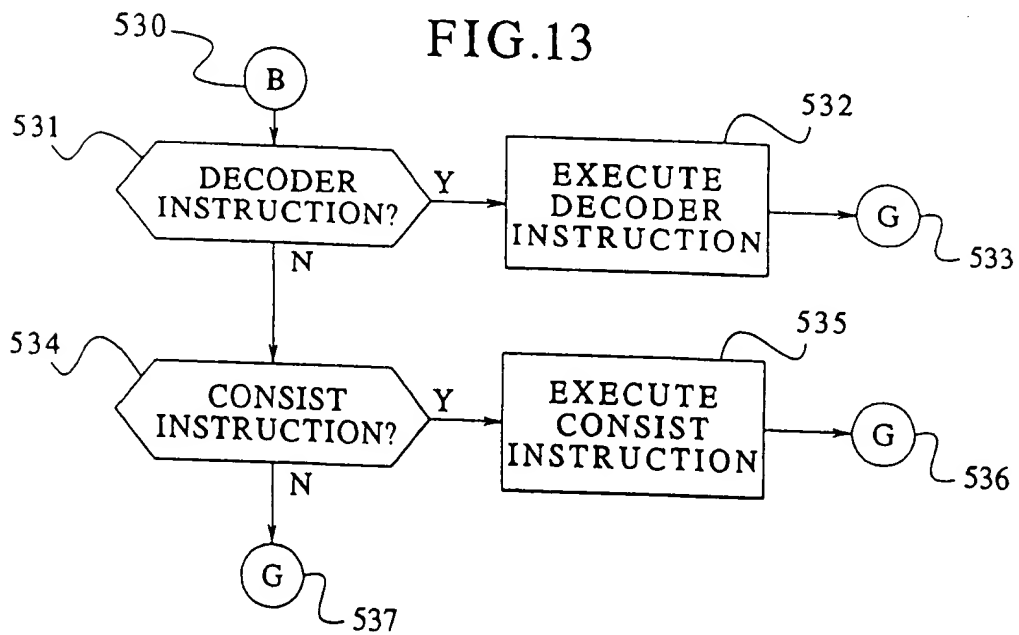


FIG.14

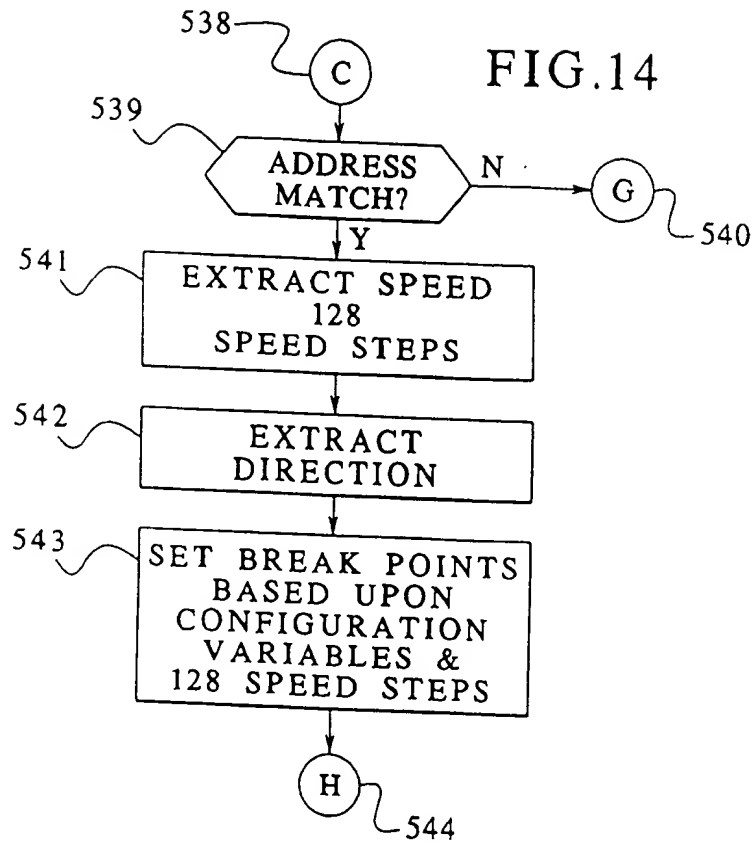


FIG.15

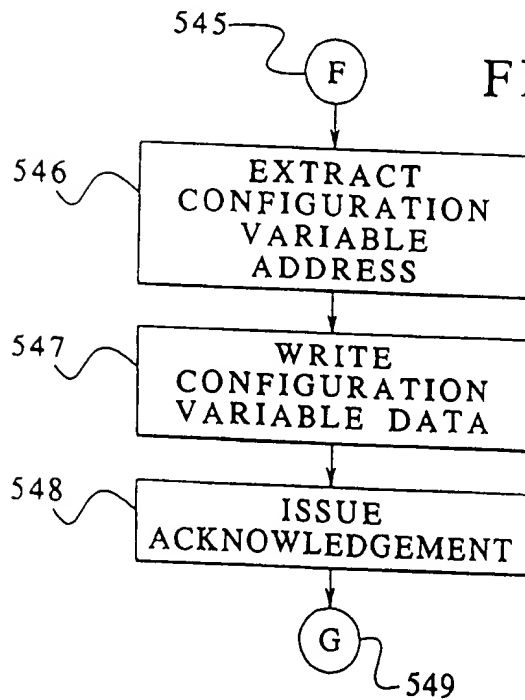


FIG.16

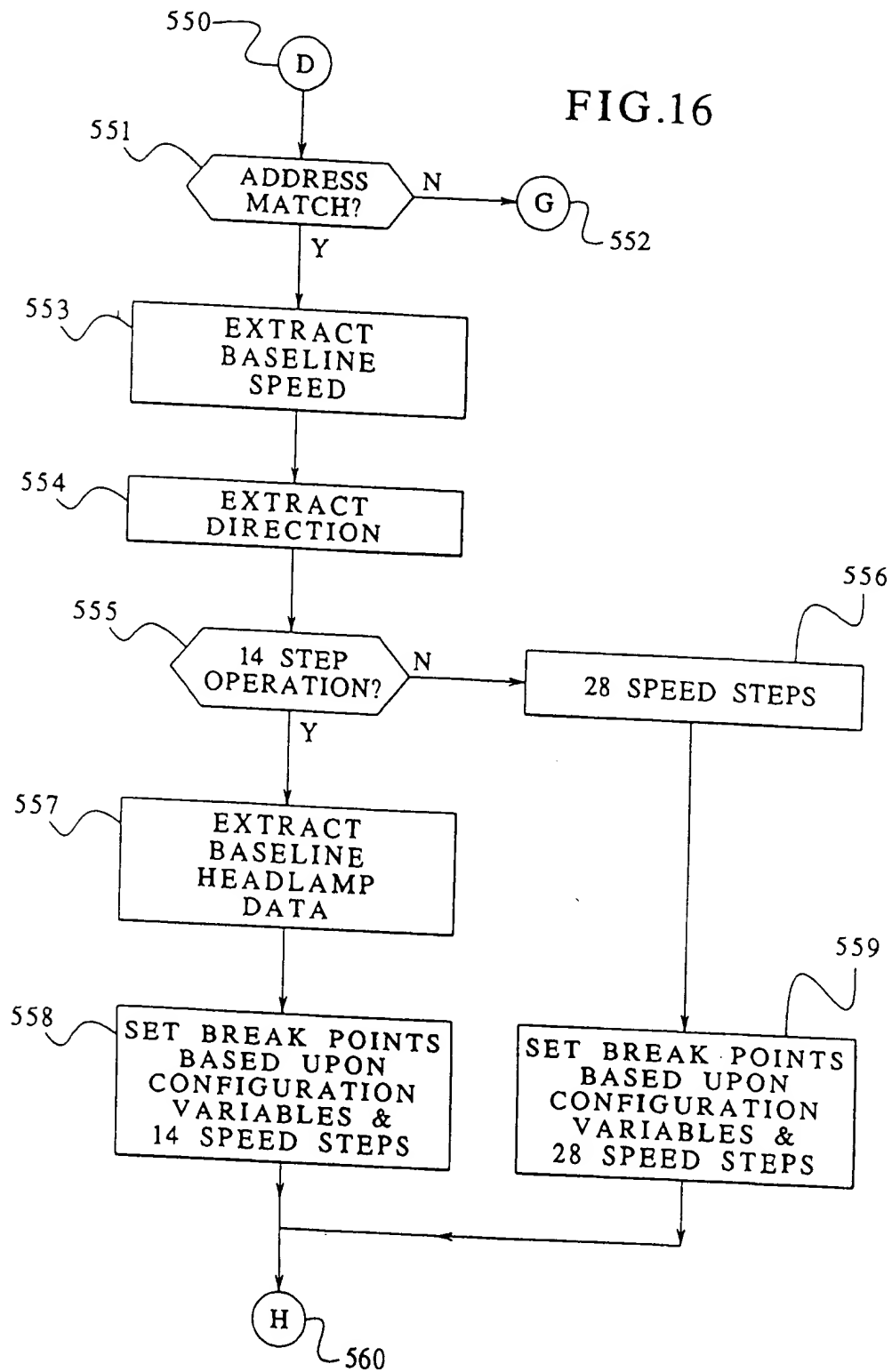


FIG.17

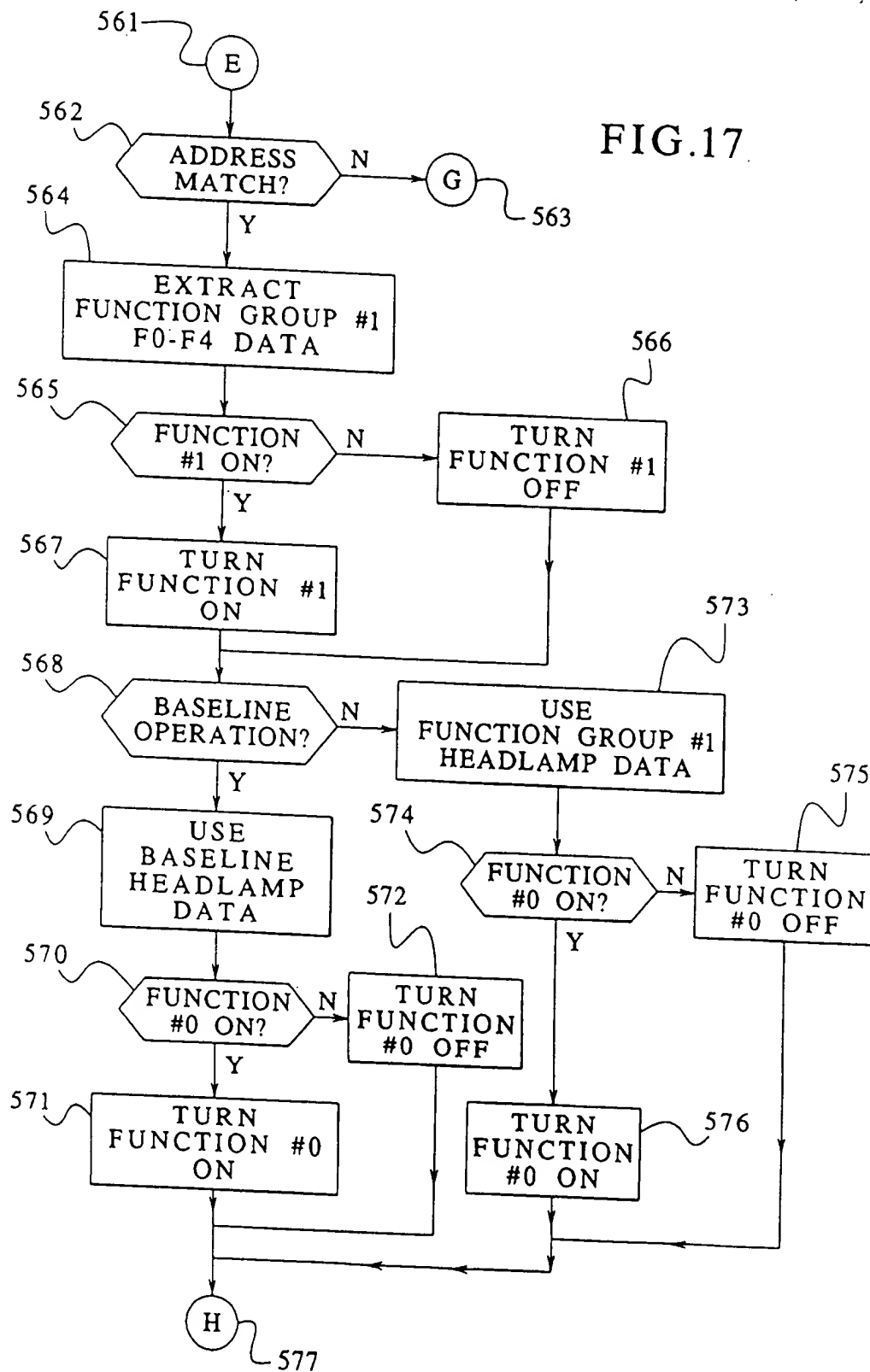


FIG.18

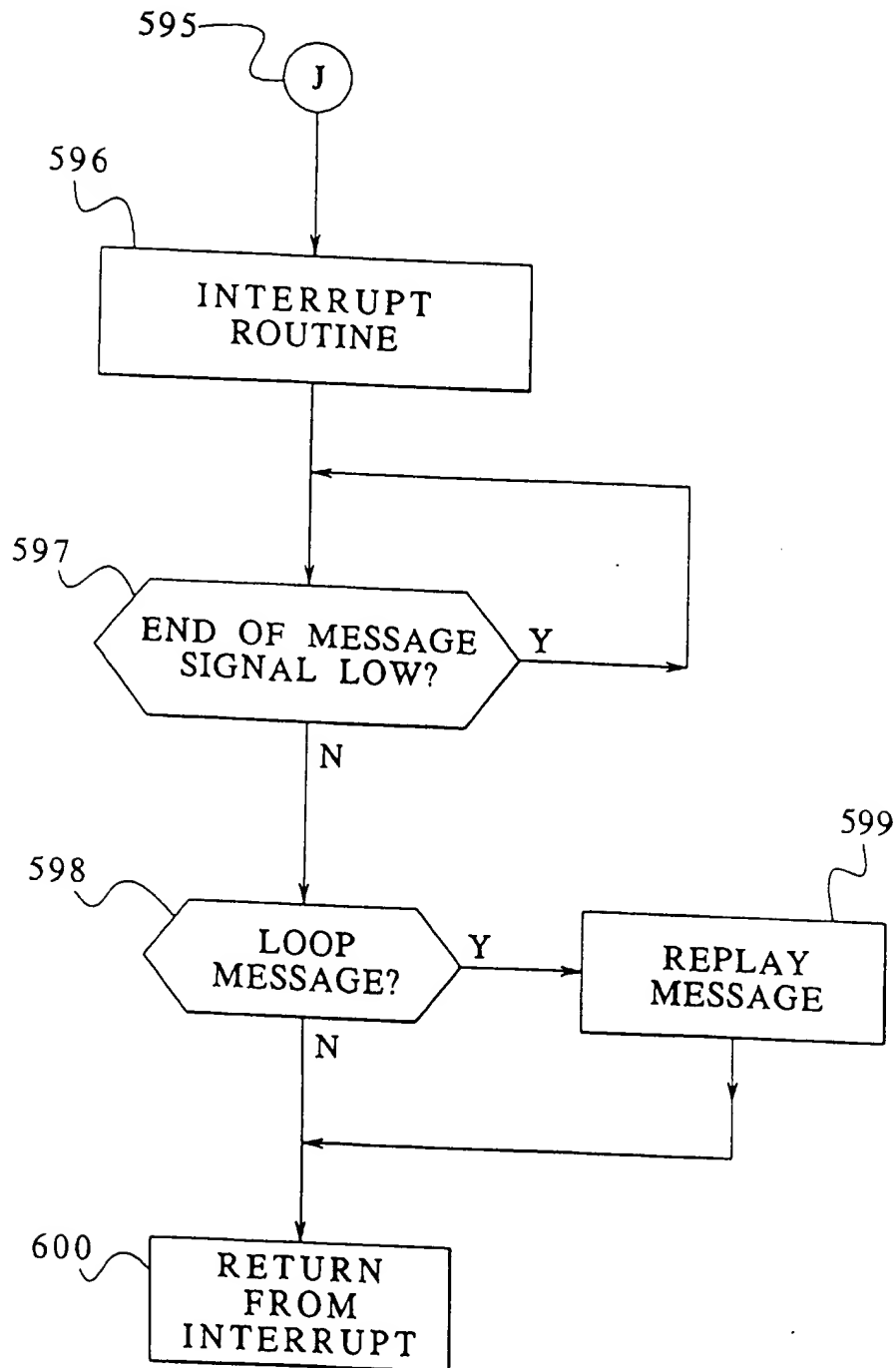


FIG.19

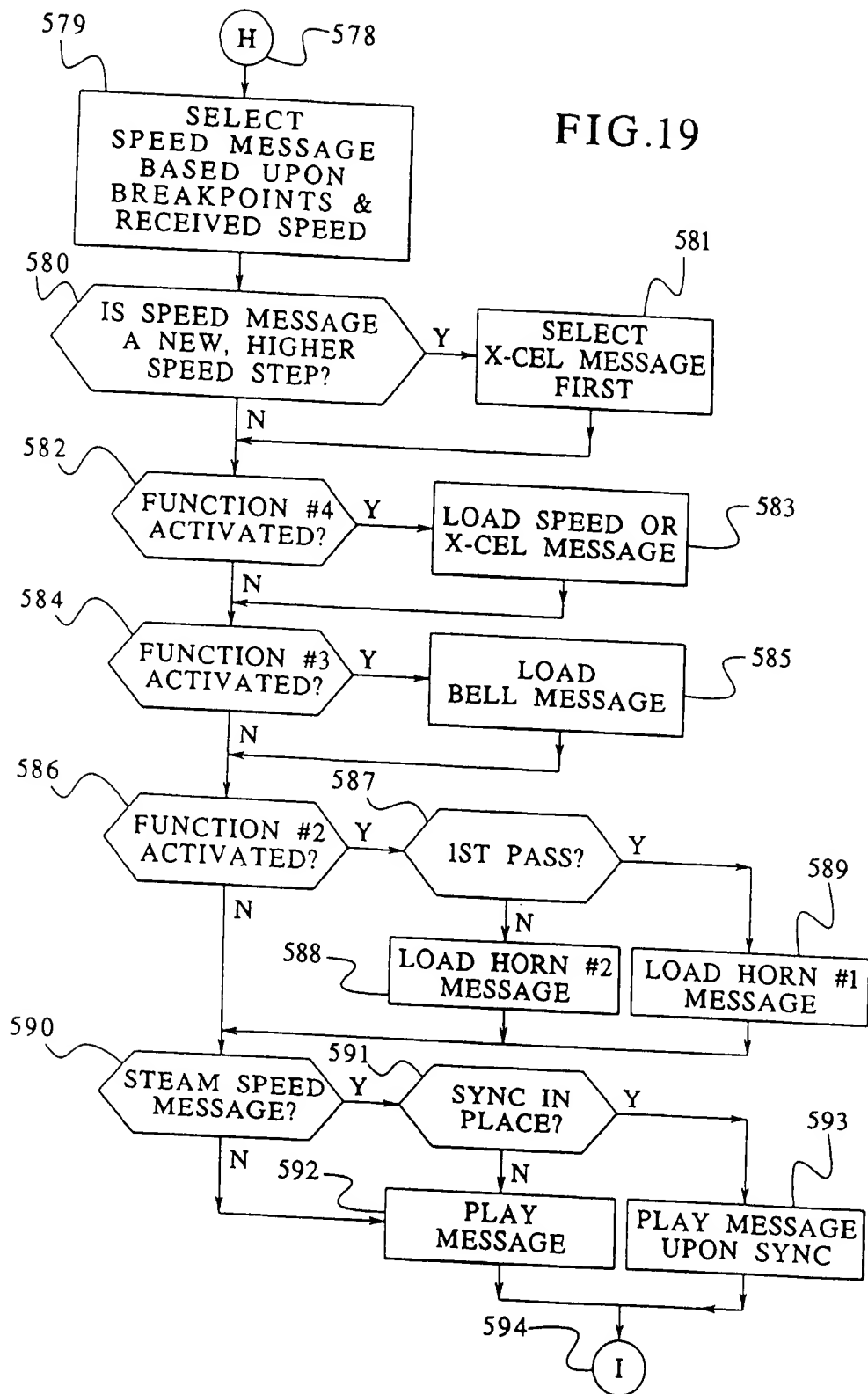


FIG.20

